Appl. No.: 10/631,84 Attorney Docket: ET01-010 Amdt. Dated: April 20, 2005

Reply to Office action of April 6, 2005

Abstract

[0025] A particular input buffer receiver includes a buffer input portion for receiving an input signal, a large capacitor between a bias node and the lower supply voltage, and a buffer output portion for producing an output signal. The circuit works to remove ground noise by charge coupling the bias voltage developed at the bias node to the lower supply voltage of the input device.

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Copy of Reference Describing Coupling Coefficient: